

Docket No.: AOK-0227

(PATENT)



# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Junya Naito et al.

Application No.: 10/502,289

Filed: July 22, 2004

For: CERAMIC PACKAGE AND CHIP RESISTOR

AND METHODS FOR PRODUCTION OF

THE SAME

Confirmation No.: 1849

Art Unit: 2814

Examiner: Theresa T. Doan

# REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop PCT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

- 1. Attached is a copy of the official filing receipt received from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested. Also enclosed is a copy of the Declaration.
- 2. There is an error with respect to the following, which is incorrectly entered.

Error in

Correct data

1. Title

1. CERAMIC PACKAGE AND CHIP RESISTOR AND METHODS FOR PRODUCTION OF THE SAME

3. The correction is due to error by the USPTO. However, if any costs are involved, please charge Deposit Account No. 18-0013.

Dated: May 8, 2006

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**CONFIRMATION NO. 1849** 

Date Mailed: 12/28/2004

**FILING RECEIPT** 

\*OC000000014834325\*

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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

# Applicant(s)

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#### Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/14753 11/19/2003

## Foreign Applications

JAPAN 2002-341300 11/25/2002

Projected Publication Date: 04/07/2005

Non-Publication Request: No

Early Publication Request: No

Title

Ceramic package and chip resistor, and method for manufacture thereof

**Preliminary Class** 

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